Good afternoon!

It is really my honor to have this opportunity for an interview, I hope I can have a good performance today. Now I will introduce myself briefly.

My name is Wuwanqiang. I am 26 years old, born in Xinyang city, Henan province. I will graduate from Southwest Minzu University. The major is Electronic and Communication Engineering. I have passed CET4/ NCRE. During the master's degree, I spent most of my time in FPGA engineering development, which is also my favorite direction.

In the past three years, I have done three projects related to FPGA development. The first is a non-invasive load monitoring system based on SOPC. The second is the OUT32M disk design of the time-frequency synchronization[ˌsɪŋkrənaɪ'zeɪʃ(ə)n] integrated equipment. The last one is the research on time keeping algorithm[ˈælɡərɪðəm] of time frequency server, which is also the topic of my graduation thesis.

Although I don't have enough experience, I am willing to study hard in order to enter the field of chip design.

That's all. Thank you for giving me the opportunity.